AVR1900: Getting started with ATxmega128A1 on STK600

1 Introduction

This document contains information about how to get started with the ATxmega128A1 on $STK^{\$}600$.

The first three sections contain information about the software (AVR Studio[®], WinAVR and IAR Embedded Workbench[®] for AVR[®]) needed to compile and debug code for ATxmega128A1. The next section contains a brief overview on how to set up the STK600 for ATxmega128A1. The final section shows how to create a project and compile code for ATxmega128A1 using IAR Embedded Workbench or WinAVR, and how to start debugging the code on the STK600 using AVR Studio and JTAGICEmkII.

More Information about where to find the software tools can be found in chapter 7.



8-bit **AVR**[®] Microcontrollers

Application Note

Rev. 8107A-AVR-04/08





2 AVR Studio that supports ATxmega128A1

AVR Studio is used to debug code. It contains a simulator and a frontend for the JTAGICE mkll for doing on chip debugging on AVR XMEGA. To have support for STK600 and ATxmega128A1, AVR Studio version 4.14 or later must be installed.

Note: During installation of AVR Studio remember to install the USB driver.

3 Installing IAR Embedded Workbench 5

IAR Embedded Workbench 5.10 (or later) for AVR (IAR EW Evaluation and Full version) has compiler support for ATxmega128A1.

To start the installation use the autorun application, then select "Install IAR Embedded workbench".

IAF	R Embedded Workbench®		
	information		
X	Install IAR Embedded Workbench®	Install IAR Embedded on your computer. The project handler, edito	Workbench® his includes
h t m	View the release notes	assembler, linker an	d debugger
A	Ween the product documentation		
s	install the Adobe Acrobat Reader		
	Explore the ED		
°	1		
			O IAR
	wwv	v.iar.com	SYSTEMS

Figure 3-1: Installing IAR EW for AVR.

During installation it is possible to use the same License number and License key as for previous versions of IAR Embedded Workbench.

It's recommended to use the default settings during the installation of IAR. How to start a project in IAR EW is presented in chapter 6.1.

4 Installing WinAVR

Download WinAVR release 20080411 (or later) from Sourceforge. Run the installer and follow the instructions. The avr-gcc toolchain will integrate with AVR Studio automatically. How to start a project in WinAVR is presented in chapter 6.2.

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5 Setting up STK600 for ATxmega128A1

STK600 is designed to support all 8-bit AVR devices and AVR32 devices with flash. A system based on socket and routing cards is used to support different package types and pinouts on the STK600 board.





Before using the STK600 and before mounting any socket and routing card it is recommended to check that STK600 contains the newest firmware and that it has the right VTarget voltage. This can be done as described below.

Note: AVR Studio should be installed before connecting to the STK600 to ensure that the drivers are installed.

- 1. Connect a USB cable to the STK600 and turn it on.
- 2. Windows[®] will tell you that new hardware is found. Install the driver for STK600.
- 3. Open AVR Studio and cancel the startup dialog.
- 4. Start a connection with STK600 by selecting "Tools -> Program AVR -> Connect..." from the menu and select the STK600 and USB as illustrated in Figure 5-2.

Figure 5-2: Connect to the STK600.

Select AVR Programmer		
Platform: STK500 or AVRISP JTAG ICE JTAGICE mkII AVRISP mkII AVR Dragon STK600 Tip: To auto-connect to the program button on the toolbar. Note that a tool cannot be used for p a debugging session. In that case, s Disconnected Mode	Port: USB mer used last time, press the 'Programmer' programming as long as it is connected in elect 'Stop Debugging' first.	Connect Cancel Baud rate: 115200 Baud rate changes are active immediately.





- 1. If a newer firmware version for the STK600 is available AVR Studio will ask if you what to do an upgrade. Follow the instructions given by AVR Studio to do the firmware upgrade.
- After the upgrade connect to the STK600 again (in the same way as 4.) and open the HW Setting page. Set VTarget voltage between 1.8 V and 3.6 V and write the value to STK600. A read back to verify that the right value was written is recommended.

|--|

STK600 in PDI mode with ATxmega128A1	×
Main Program Fuses LockBits Advanced HW Settings HW Info Auto	
Voltages	
VTarget: 3.3 V	
AREF 0: 3.30 V	
AREF 1: 3.30 V	
Read Write	
Clock Generator 8 003 MHz	
8003456	
Read Write	
- Firmware Upgrade	
Upgrade	
Getting VTARGET 3.3V OK Getting AREF0 3.30 V OK	
Getting AREF1 3.30 V OK Getting oscillator parameters OK	~

When the newest firmware and the right target voltage is set, turn off the STK600 before mounting the routing card and socket card.

The routing card used for ATxmega128A1 is "**STK600 – RC100X- 13**" and the socket card used for 100 pin devices is "**STK600 – TQFP100**"

How to mount the routing card and socket card to the STK600 is shown in Figure 5-4. It is not possible to mount the routing and socket card in a wrong orientation.

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Figure 5-4: How to mount the routing and socket card to the STK600





When mounting the device in the ZIF socket ensure it is mounted in the correct orientation. Pin one on the device is marked with a dot. As seen on Figure 5-5 this corner must be orientated in the same direction as the cut corner on the socket.



Figure 5-5: Mounted device in ZIF socket

The STK600 can source power trough the USB cable. Remember that the power available trough the USB cable is limited. If the STK600 is connected to external hardware that consumes more than 300mA, you should use an external power source connected to the DC input socket on STK600. The external power supply should be 10-15V DC with **positive center** connector.

Debugging of the code is done with AVR Studio and JTAGICEmkII. How to do On-Chip Debugging using the JTAGICE mkII to STK600 is described below:

1. Connect the JTAGICE mkll to the STK600 as show below:

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Figure 5-6: Connecting JTAGICE mkll to STK600.

2. Turn on power on STK600 and JTAGICE mkII. (If it is the first time JTAGICE mkII is used, Windows will ask you to install the driver.)

More information about STK600 and JTAGICE mkII is available in AVR Studio. (Help -> AVR Tools User Guide).

6 Compiling code and debugging code on chip

This section describes how to make a project, compile and debug on-chip with both IAR EW and WinAVR. For development with IAR EW read chapter 6.1. For development using WinAVR read chapter 6.2.

6.1 Compiling and debugging with IAR and AVR Studio

To start a project for the ATxmega128A1 device in IAR EW AVR do as described below:

- Open IAR Embedded Workbench
- Select "Project -> Create New Project.." in the menu
- Select the desired coding language with AVR Studio 4 compatible output.





Figure 6-1: Create new project in IAR EW for AVR

Create New Project	×
Tool chain: AVR 💌	
Project templates:	
Empty project	<
Description: C project using default tool settings including an empty main.c file. The output is compatible with AVR® Studio 4.	
OK Cano	el

- Save the project in the desired location <project location>.
- Open project options (Project -> Options or ALT+F7)
- In the General Option Category, select the Target tab and set the Processor configuration to ATxmega128A1 as shown in the figure on the next page.

Figure 6-2: Processor configuration.

Category:	
Categoly: General Options C/C++ Compiler Assembler Custom Build Build Actions Linker Debugger CCR ICE200 JTAGICE JTAGICE JTAGICE mkII Simulator Third-Party Driver	Target Output Library Configuration Library Options Heap Configu Processor configuration cpu=xm128a1, AT xmega128A1 cpu=tiny861, AT tiny861 cpu=usb1286, AT 90USB1286 cpu=usb1287, AT 90USB1287 cpu=usb162, AT 90USB1287 cpu=usb646, AT 90USB646 cpu=usb647, AT 90USB647 cpu=usb647, AT 90USB82 cpu=usb82, AT 90USB84 cpu=usb82, AT 90USB84 cpu=usb82, AT 90USB84 cpu=usb82, AT 90USB84 cpu=usb82, AT 90USB84
	OK Cancel

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• To enable bit definitions in I/O space, open the system tab and check out the "Enable bit definitions in I/O- Include files" box.

General Options C/C++ Compiler	Library Options Heap Configuration	System MISRA	c) •
Assembler Custom Build Build Actions Linker Debugger CCR ICE200	Data stack (CSTACK) Size (bytes): 0x20 Elace in external memory Egternal Memory Configuration Enable external memory bus	<u>B</u> eturn addres Size (levels): ☐ Place in	ss stack (RSTACK) 16 external memory
JTAGICE JTAGICE mkII Simulator Third-Party Driver	Add one <u>w</u> ait-state to externa RAM Base address OxO Memory size OxO	al memory accesses R <u>O</u> M OxO	Non-Volatile 0x0 0x0
	 ✓ Initialize unused interrupt vector ✓ Enable bit definitions in I/O-Inclu 	s with RETI instructi ude files	ons

Figure 6-3: Enable I/O bit definitions from include files.

- Include ioavr.h in your files that shall use I/O bit declarations.
- Compile your code pushing the "Compile" button or selecting compile from the Project menu, or simply press "Ctrl+F7"
- Make an output file for AVR Studio by pushing "Make" button or selecting make from the Project menu, or simply press "F7"
- For debugging the program make the connections as described in chapter 5. Open AVR Studio and click on the "Open" button in the startup dialog. Go to the location where you saved your project in IAR. In the project directory, locate the Debug folder, and the "Exe" inside. The "Exe" folder contains the d90 file, which is IAR's debug output file for AVR Studio. Open this d90 file in AVR Studio.





Figure 6-4: Open IAR debug output file in AVR Studio

Open Project F	ile or Object Fi	le			? 🔀
Look in:	🗀 Exe		•	🗢 🗈 💣 🎞	-
	AES_example.o	490			
My Recent Documents					
B					
Desktop					
Mu Documents					
my Documents					
My Computer					
e					
	F 1	450			0
My Network Places	File name:	ALS_example		_	Upen
	Files of type:	Project Files, Object Files (*.)	aps,*.he	x;*.d90;*.a{ 💌	Cancel

- Studio will then generate an AVR Studio Project File that needs to be named and saved.
- Select JTAGICE mkll as debug platform and ATxmega128A1 as device as shown on the next page.

Figure 6-5: Select debug platform.

Select device and de	Select debug platform and device Debug platform: AVR Dragon AVR Simulator AVR Simulator 2 ICE200 ICE40 ICE50 JTAG ICE JTAGICE mkII	Device: ATtiny461 ATtiny48 ATtiny84 ATtiny85 ATtiny85 ATtiny861 ATtiny88 ATxmega128A1 ATxmega64A1 ATxmega64A1	
Ver 4.14.585	Port: Auto 🔽 🗖 Open platfor	AT 89551 AT 89552 m options Finish Cancel	Help

When this is done the debug session is started. More information about AVR Studio and debugging in AVR Studio are found in "AVR Studio User Guide" (Help-> AVR Studio User Guide)

6.2 Compiling and debugging with WinAVR and AVR Studio

To start a project for the ATxmega128A1 device in WinAVR and AVR Studio do as described below:

- Open AVR Studio
- Select "Project -> New Project" from the menu
- Select AVR GCC in the pop-up window and type in the project name and file as shown below:

Atmel AVR Assembler	myproject ✓ Create initial file Initial file: myproject .c	
Location: C:\		

Figure 6-6. Create new AVR GCC project

 Press "Next>>" and select "JTAGICE mkII" in the Debug platform, and "ATxmega128A1" as Device:





Figure 6-7. Debug platform and device

addo 4	Select debug platform and device Debug platform: AVR Dragon AVR Simulator AVR Simulator 2 ICE 200 ICE 40 ICE 50 JTAG ICE JTAGICE mkII	Device: ATtiny461 ATtiny48 ATtiny84 ATtiny85 ATtiny861 ATtiny88 ATxmega12841 ATxmega64A1 AT868F401	
Ver 4.14.585	Port: Auto 🔽 🗖 Open plat	AT89551 AT89552 form options Finish Cancel	Help

- Press "Finish" and the project will be created
- Select "Project -> Configuration Options" from the menu
- In the configuration window, select "atxmega128A1" in the device option and select the wanted optimization level and press the OK button.

Figure 6-8. WinAVR Project options

myproject Project Options		
500)	Active Configuration default	
General	Use External Makefile Target name must equal project name. Clean/rebuild support requires "clean" target. Makefile and target must exist in the same folder	
Include Directories	Output File Name: myproject.elf	
	Output File Directory: default	
Libraries	Device: abxmega128a1 Image: Constraint of the state o	
Memory Settings	Optimization: Os Pack Structure Members (fpack-struct) Short Enums (fshort-enums)	
Custom Optior	✓ Create Hex File ✓ Generate Map File ✓ Generate List File	
	OK Cancel Help	

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The project is now set up, and you can write your program in the C file. To compile the program and debug it on the device, make the same connections as described in chapter 5. Compiling your program can be done as follows:

- To compile only the active file, select "Build -> Compile" from the menu or pushing the "Compile" button, or by simply pressing "Alt+F7".
- To make the whole project, select "Build -> Build" from the menu or by pushing the "Build" button, or by simply pressing "F7"
- The project is now build and you can debug the code on-chip by selecting "Debug -> Run" from the menu or by pushing the "Run" button, or by simply pressing "F5".
- The two previous steps can be done simultaneously by selecting the "Build -> Build and Run" form the menu or by pushing the "Build and Run" button, or simply by pressing "Ctrl+F7".

Your debugging session has now started and you can debug the program on-chip. For more information about debugging, read the "Help -> AVR Tools User Guide" in AVR Studio.





7 More Information

For more detailed information about the ATxega128A1 device, read the datasheet and the XMEGA A Manual available at the Atmel[®] web pages:

- ATxmega128A1 datasheet:
 - o http://www.atmel.com/dyn/resources/prod_documents/doc8067.pdf
- ATxmega A Manual:
 - o http://www.atmel.com/dyn/resources/prod_documents/doc8077.pdf
- XMEGA Application Notes:
 - http://www.atmel.com/dyn/products/app_notes.asp?family_id=607#X
 MEGA%20Microcontrollers

Tools needed for development can be found here:

- AVR Studio
 - o http://www.atmel.com/dyn/products/tools_card.asp?tool_id=2725
- IAR Embedded Workbench (Evaluation or Full version)
 - o http://www.iar.se/
 - o http://supp.iar.com/Download/SW/?item=EWAVR-EVAL
- WinAVR
 - o <u>http://winavr.sourceforge.net/</u>
 - o http://sourceforge.net/project/showfiles.php?group_id=68108



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